

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	9	(multi-chip\$1 or multichip\$1) and ((programmable NEAR1 \$5connect\$4) or (programmable NEAR1 rout\$4)) and ((support\$4 adj1 structure) or (carrier NEAR1 die\$1) or (carrier NEAR1 substrate\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/16 11:34
L2	1035	(multi-chip\$1 or multichip\$1) and ((programmable adj1 logic) or pld\$1 or fpga)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/16 11:13
L3	78	2 and ((programmable NEAR1 \$5connect\$4) or (programmable NEAR1 rout\$4)) and (pad\$1 or pin\$1) and (driver\$1 or buffer\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/16 11:16
L4	5	3 and ((support\$4 adj1 structure) or (carrier NEAR1 die\$1) or (carrier NEAR1 substrate\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/16 11:34

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L1	0	((multi-chip\$1 or multichip\$1) and ((programmable NEAR1 \$5connect\$4) or (programmable NEAR1 rout\$4)) and ((support\$4 NEAR1 struct\$3) or (carrier NEAR1 die\$1) or (carrier NEAR1 substrate\$1)) and (driver\$1 or buffer\$1)).cim.	US-PGPUB	OR	OFF	2006/03/16 12:08